WHAT IS CLAIMED IS:

1. A system for updating the time and date of all of the electronic devices within the system, the system comprising:

a communications network being coupled to each of said electronic devices within said network; and

at least two electronic devices, wherein each electronic device has a time and date set feature capable of being set by a user;

wherein any one of said at least two electronic devices is configured to communicate a time and date set function to any respective electronic device after having received a set instruction until all devices within said communications network have been set.

2. The system as recited in claim 1, wherein said communications network comprises a communications module which utilized standard communications protocol to communicate time and date set data between said electronic devices within said communications network.

3. The system as recited in claim 2, wherein said communications network comprises a Programmable Logic Controller.

4. The system as recited in claim 1, wherein said time and date set feature is a time code.

5. The system as recited in claim 1, wherein said time and date feature is a date code.

6. A process for updating the time code and date code of the devices within a communications network, wherein each device comprises a microprocessor, a communications module, memory, and a key pad, the process comprising the following steps:

reading the time code from memory;

sending the time code to the communications controller;

reading the date code from memory;

sending the date code to the communications controller; and

the communications controller sending time and date information to all of the electronic devices within the network.

7. The process as recited in claim 6, further comprising the step of reading the time and date information from memory upon execution of a clock setting routine;

8. The process as recited in claim 6, further comprising the step of reading time and date information from memory after a clock set keypad entry function has been initiated.

9. The process as recited in claim 6, further comprising the step of reading time and date information from memory after the communications module transmits an interrupt signal to the microprocessor.

10. The process as recited in claim 7, wherein said time and date information is a time code.

11. The process as recited in claim 7, wherein said time and date information is a date code.

12. An apparatus for updating the time code of all of the appliances within a communications network having a communications controller, wherein the time variable comprises a time code and a date code, the apparatus comprising:

means for reading the time variable;

means for sending the time code to the communications module;

means for sending the date code to the communications module; and

means for the communications module sending the time variable to the devices on the network.

13. A system for updating the time and date of all of the appliances within the system, the system comprising:

a communications network being coupled to each of said appliances within said network; and

wherein at least two appliances each has a time and date set feature capable of being set by a user;

wherein any one of said at least two appliances is configured to communicate a time and date set function to all respective appliances within the network after having received a set instruction.

14. The system as recited in claim 13, wherein said communications network comprises a communications module which utilized standard communications protocol to communicate time and date set data between said appliances within said communications network.

15. The system as recited in claim 14, wherein said communications network comprises at least two Programmable Logic Controllers.

16. The system as recited in claim 13, wherein said time and date set feature is a time code.

17. The system as recited in claim 13, wherein said time and date feature is a date code.

18. A process for updating the time code and date code of the appliance within a communications network, wherein each appliance comprises a microprocessor, a communications module, memory, and a key pad, the process comprising the following steps:

means for reading the time code from memory;

means for sending the time code to the communications module;

means for reading the date code from memory;

means for sending the date code to the communications module; and

means for the communications module sending time and date information to the appliances within the network.

19. The process as recited in claim 18, further comprising means for reading the time and date information from memory upon execution of a clock setting routine.

20. The process as recited in claim 18, further comprising means for reading time and date information from memory after a clock set keypad entry function has been initiated.

5